

PC becomes an FA controller
without interface function

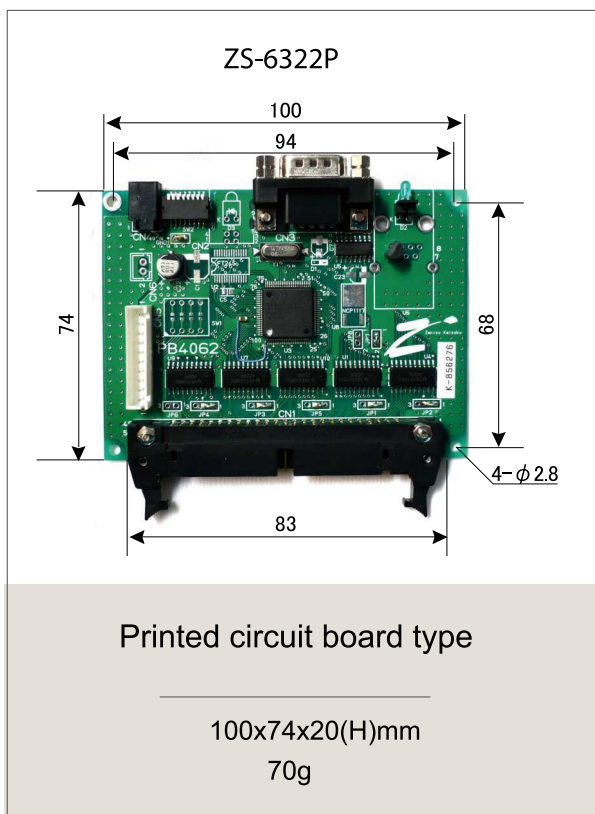
RS-232C Adapter

ZS-6322P/S

Compliant with RoHS

ZS-6322P/S is small and easy to handle RS-232C adapter that enables digital signal to communicate with RS-232C interface.

Digital signals such as BCD and Binary are possible to be imported to the personal computer and can be ON/OFF controlled easily from the personal computer.

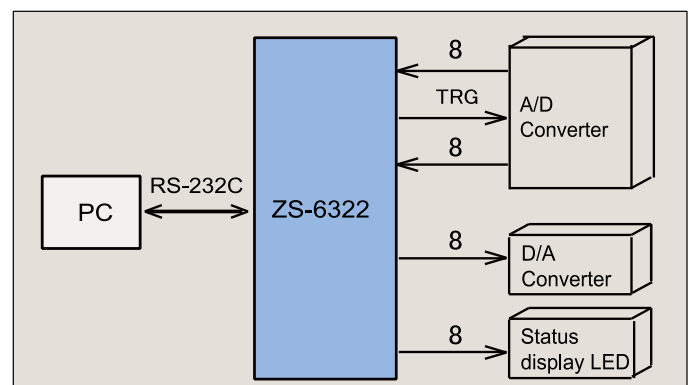


Feature

- Digital I/O 32 bits
It can be selected I/O with byte unit.
- DIO interface (74AC245) has enough output drive.
- The operation mode is set by command.
- Small and easy to use.
- Value pricing.

Example for usage

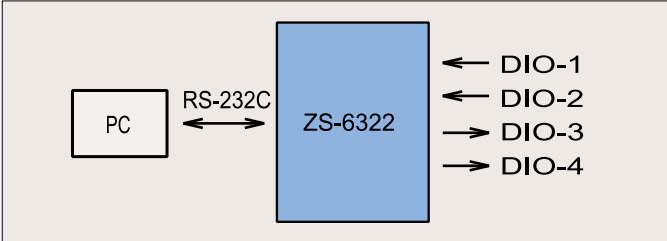
This is example for usage, 16-bit A/D converter input, 8-bit D/A converter output and 8-bit status display LED.



I/O functions

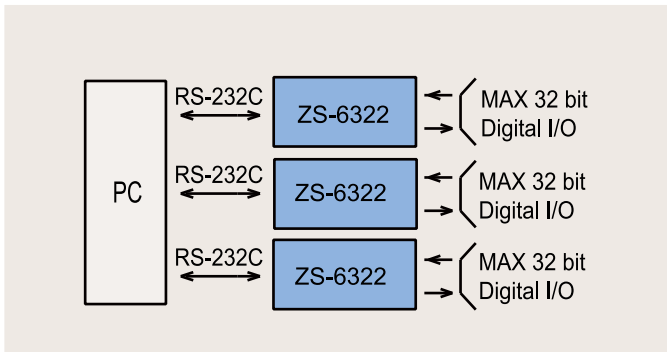
ZS-6322 is possible to be supported digital input/output signals up to 4 bytes.
I/O selection is set with byte unit using software.

e.g) ZS-6322 is used for 2-byte input and 2-byte output.



Multiplex connection is available

Multiple ZS-6322 can be controlled with one computer by switching COM port.



Control signal

The control signals shown in the table below are prepared so that RS-232C adapter can synchronize with the connected equipment.

Name	Signal		Description
	Direction	Type	
STB	OUT	P	It outputs a pulse signal after outputting the data received from the RS - 232C to output port.
TRG	OUT	P	It outputs pulse signal by T command.
CLR	OUT	P	It outputs pulse signal by C command.
LAH	IN	P	Data input is latched with the negative pulse of this signal when the latch circuit is enabled. Minimum pulse width 500 μs.

Note) P of the output signal is available to be set pulse width with P command.

Command

ZS-6322 considers the first 1 byte sent from the PC as a command and processes with followed the character strings. The data is transferred in 2-digit units by HEX code.

Command	Discription
W	Data output.
R	Data input.
T	Pulse output with TRG signal. Pulse width is set by P command.
C	Pulse output with CLR signal. Pulse width is set by P command.
D	I/O setting with byte unit. Output with "O" of OUT, input with "I" of IN is specified and 4 digit character string is output.
P	Set the pulse width of the control signal with one digit of 0 to 2. There are Three type of pulse widths 10 μs, 100 μs, 1 ms
L	Latch circuit is set to enable or disable when the data input. (1: Enable 0: Disable)

Specifications

Full duplex communication method
Asynchronous method
Communication speed : 2400 4800 9600 19.2kbps
Parity: None, Odd, Even
Stop length: 1, 2
Character length: 7, 8
Delimiter: CR, CR+LF
Connector: Dsub 9pin (RDEB-9P or equivalent)
Amount of data: 32bits (4bytes)
I/O Level: TTL signal
(Driver IC SN74AC245 or equivalent)
Control input: LAH
Control output: STB, TRG, CLR
Connector: 50-core FC connector
(FAP-5001-1202-0BF or equivalent)
Power supply: DC5V less than 100mA
Accessory: Data connector
(50-core FC connector)

Option

AC Adapter: [GF12-US0520](#)
Input: AC100 to 240V Output: DC5V 2A
Small switching method that does not take space with a table tap.
RS-232C Cable: [KR-ECL9-2](#)
DTE (Dsub 9S) <=> DOS/V (Dsub 9S) Cross 2m

Please contact us for other lengths and standards as well.

Specifications and appearance are subject to change without notice due to continual improvements.



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